DEPARTMENTOF ELECTRONICS AND COMMUNICATION ENGINEERING
MINUTES OF XV BOARD OF STUDIES MEETING CONDUCTED

ON 24.03.2018

The Department of Electronics and communication engineering conducted Board of studies meeting for approving curriculum R2018 and syllabus R2018 of M.E APPLIED ELECTRONICS programme on 24.03.2018 (Saturday) at 03.00 pm in 'C' Block conference hall.

Dr.S.A.Pasupathy, Professor and Head of the Department chaired the meeting and Prof. K.Ramprakash Professor (Head, PG Programs) presented the draft version of curriculum and syllabus.

The following Members attended the meeting

Sl.No.	Nameofthe member	Role of the Member
1.	Dr.S.A.Pasupathy Professor and Head	Chairman-BOS
2.	Dr.Y.V.Ramana Rao Professor, Department of Electronics and Communication Engineering, Anna University, Chennai -600025. Ph.No:044 22358883 Email.Id:yvramanarao@hotmail.com	Anna University Nominee
3.	Dr.H.Mangalam , HOD, Department of Electronics and Communication Engineering, Sri Ramakrishna Institute of Technology, Coimbatore. Mob.No: 9787812624 Email: hmangalam2@gmail.com	Experts nominated by Academic Council (outside the college)
4.	Dr.P. Vijayalakshmi, Professor Department of ECE, SSN College of Engineering, Old Mahabalipuram Road, SSN Nagar, Kalavakkam- 603 110 Email: vijayalakshmip@ssn.edu.in Mob. No: 91 44 27469700	

5.	Mr. Divyanshu Verma, Senior Manager, Intel R&D, Bangalore.	Representative from Industry
	N.Kavitha Director, Steps Knowledge Services (P) Ltd, "ShriNivas"26, 2nd Street, Kasthuri Nagar, Ondipudur, Coimbatore-641016. Mob.No: 8098622931, Email.Id: skt@stepsmail.com	
6.	Mr.Senthil Govind Shetty, Senior Manager, Robert Bosch Engineering and Business Solutions, Coimbatore. Email:senthil.g@in.bosch.com Mob: 9585546044	Alumni nominated by principal
7.	Dr.C.Vasanthanayaki Professor, ECE, Government College of Technology,Coimbatore. Mob.No:9487564005 Email.Id:vasanthi@gct.ac.in	Special Invite
8.	Prof.S.Kaliappan Assistant Professor III, Department of EEE,KCT	
9.	Dr.MaryCheriyan Head AcedemicAdminstration Kumaraguru College of Technology, Coimbatore Phone:+91-9442002392 Email ID: marycherian@kctbs.ac.in.	
10.	Prof. K.Ramprakash Professor **(Head, PG Programs)** Dr.K.Kavitha, Professor (Convenor) Dr.K.Paramasivam, Professor Ms M Shanthi, Associate Professor Dr.A.Amsaveni, Professor Ms.R.Hemalatha, Associate Professor Ms.S.Sasikala, Associate Professor Dr.M. Alagumeenaakshi, Associate Professor	Faculty from each specialization
11.	Ms. K. Thilagavathi Assistant Professor III Ms.S. Nagarathinam Assistant Professor III Ms. A Kalaiselvi Assistant Professor II Mr. S. Karthik Assistant Professor I Mr. S. Arun Kumar Assistant Professor I	Other members of staff of the same department
12.	Dr. J. Vijay Prakash Deputy Controller of Examinations & Associate Professor Kumaraguru College of Technology	COE/Coordinator Autonomous
13.	Dr.Vinohar Stephen Rapheal Head Academic Innovation, Kumaraguru College of Technology, Saravanampatti, Coimbatore Email.Id: vinohar.stephenrapheal.bt@kct.ac.in	DOA

The following recommendations are suggested by the BoS expert members

- **Analog Integrated Circuits Design** may be shifted to II semester
- The course content of **Applied Signal Processing** can be updated by removing topics related to Spectrum estimation, Kalman Filter, Backward and forward prediction filters and add topics related to Electromagnetic Interference. Lab provided as embedded lab for Applied Signal processing can be removed and can be included with other lab using MATLAB software.
- It was suggested that the **Advanced Processors** title can be changed to RISC and CISC Processor including topics to ARM Cortex M4 and Pentium processor elaborately.

- In **VLSI Signal Processing**, suggested to remove the following content
  Solving Systems of Inequalities
  Module II – Systolic Architecture and
  Elaborate Module V, including Multiplier Architecture.
- It has been suggested to remove the multiple Text and Video compression standards and discuss the latest JPEG and MPEG standards. Also QMF and Subband coding concepts is not needed.
- It is suggested to have separate courses as
  1. **Soft computing and optimization techniques**
  2. **Artificial Intelligence**
- In **Advanced Digital Design**, FPGA can be explored in place of PAL/PLA/CPLD.
- In **Advanced Computer Architecture**, they suggested to reduce the number of Cache optimization techniques and concentrate on recent architectures.
- It has been suggested to reduce and change the syllabus for **Robotics and Industrial Automation.**
- It has been suggested to shuffle the order of the units in **Nano electronics.**
- In **Hardware Verification Techniques**, include OVM in fifth module
- In **Research methodology**, they suggested to incorporate practical methods to improve writing skills, how to do research and selling of science.
- Paper Publication was made mandatory as per KCT norms for ME student to get pass in their project.
- In general, topics like **optimization, random variables** can be added in appropriate courses for PG Programmes.

Head- PG Programmes

HOD ECE